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EDUCATION

BTech in Electronics & Communication.

Saintgits college of engineering Pathamuttom

08/2016 - 08/2020

Kottayam

Courses

- Analog Communication, Digital Communication, VLSI, Control Systems, OOP
- Electronic Circuits, Signals and System, Embedded Systems, microcontrollers.



SKILLS AND ABILITIES

Research & Strategy

Troubleshooting

Problem solving

Active Listening

Presentation

Leadership

Team work

IoT

Java

TTCN

HTML&CSS

C

C++

Java script

Linux OS



WORK EXPERIENCES & PERSONAL PROJECTS

Amantya Technologies (02/2022 - Present)

- Served as Software Developer.
- Deals with 4G and 5G stack development for Verizon.
- Plays key role in developing 4G Stack side adapter for connecting L2 and L3 Layers.
- Deep understanding about L1, L2 & L3 layers, TTCN, SA, eNB
- Demonstrated expertise in testing testcases according to the testplan.
- Proficiently troubleshoot simple and complex jira issues for 4G.

Tayana Software Solutions (10/2021 - 02/2022)

- Served as a Software Developer.
- Worked with UPCC and PCRf team.
- Worked with developing TashiCell (Butan 5G Network).

SIFLI (Smart Intravenous Fluid Level Indicator) (2020)

- The aim of the project is to reduce the hassle of nurses by automating the process of periodic checking of the fluid levels of drip.
- Lead a team of 4 and was instrumental in completing SIFLI by designing a new device that could reduce the workload.
- Used and applied microcontrollers, sensors, programming and circuit designing skills to design and develop the product.
- Paper Published in International Research Journal (IRJET).
- Presented in International Conference. (I CASE)

Efficient LoRa GPS Tracker for Dementia Patients (2019)

- To propose a novel energy-efficient, small wristband by integrating the latest LoRa communication and GPS duty cycling technologies. Our model and data show the GPS wristband is able to support up to 40 hours of continuous GPS tracking with a frequent 60 seconds location update rate. Its range also spans 3km, effectively monitoring patient locations.
- Finalist in State level YIP.

Rapid Emergency Distress Signal (2018)

- To better improve the emergency communication and location-based services of disaster information reporting network for a serious natural disaster emergency response and relief, we propose a solution based on LoRaWAN networks, that enables individuals to alert the rescue authorities during the crisis in a highly efficient manner.
- Finalist in Paper presentation competition conducted by NIT Calicut.

Mosquito Magnet (2015)

- The project aims to develop an eco-friendly mosquito eradication system.
- Lead a team of 5 and successfully designed and developed a natural liquid that is an alternative for the chemical liquid we use today, and an improvised mechanism for source termination.
- Finalist in Yuva Master Mind conducted by Malayala Manorama.
- Finalist in Techtop.



ACHIEVEMENTS

5 star coder

HackerRank - 5 star Gold medal in C++

YOUNG INNOVATORS PROGRAM (YIP) (2019)

Tracker for Dementia Patients. State level innovators competition. One of the top ten finalist held in Kannur

Paper Published in International Research Journal (IRJET)

SIFLI (Smart Intravenous Fluid Level Indicator)

Paper Presentation (2018)

Got selected for the finals paper presentation Conducted as a part of NIT Tech Fest.

Technical quiz competition. (2017)

Conceived First prize, Conducted by IEEE saintgits

Techtop. (2015)

National innovation contest for Engineering students held in Thiruvananthapuram.

Yuva MasterMind (2015)

National level innovation competition Finalist.

Southern Indian Science fair. (2014)

All India science competition finalist Conducted by Tamil Nadu Government in Chennai

State science fair. (2013)

Bagged second prize in state level science competition Conducted by Kerala State Government in Kannur



INTERNSHIP & COURSES

ISRO (2014)

Student program held in connection with world space week celebration by VSSC

BSNL (2017)

Basic Telecom (2 weeks). Held in Thiruvananthapuram.

Coursera

Reliable Google Cloud Infrastructure: Design and Process

Matlab & Ansys - HFSS (2014)